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| First Inventor: | Rajiv Jain et al.                 |                 |               |
| Assignee:       | QuickLogic Corporation            |                 |               |
| Title:          | Method of Programming an Antifuse |                 |               |
| Serial No.:     | 09/887,834                        | Filing Date:    | June 22, 2001 |
| Examiner:       | Terry Cunningham                  | Group Art Unit: | 2816          |

**Enclosed for filing in the U.S. Patent and Trademark Office for the above-identified application are the following:**

- 1. Power of Attorney; Revocation of Previous Powers; and Statement under 37 CFR 3.73(b) (1 page); and**
- 2. Status Inquiry (1 page)**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**First Inventor: Rajiv Jain et al.**

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